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### A STUDY OF PERMUTATION ADMISSIBILITY WITH COLORED PETRI NETS

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#### ABSTRACT

The permutations, which are routable through a multistage interconnection network without any conflict (known as the admissible permutations) play important role in determining the capability of the network. Admissibility of frequently used permutations becomes a crucial issue when we decide on best matching between parallel algorithms and architecture of parallel computers.

In this paper, we propose an innovative approach to analyze the permutation admissibility. The approach is centered upon colored Petri net modeling of multistage interconnection network with further analysis of associated model through exploiting colored place invariants.

### I. INTRODUCTION

Rapid communications among processors are important for parallel computers. Permutations are frequently used communications. For example, data are permuted among processors for the next step of parallel algorithm or data are scrambled before they are stored in the parallel memory. Permutation can be viewed as a one-to-one and onto mapping of sources to desired destinations. A permutation is said to be admissible to MIN if conflict-free paths can be established for all input-output pairs simultaneously. Many kinds of MINs have been investigated for permutation admissibility. Over the years researchers have investigated the admissibility of linearpermutation-class (LIN) and bit-permutation-class (BPC) permutations to shuffle-type [4-6], cube-type [3,7] and hypercube [8] networks. These works are mainly centered upon use of algebraic techniques or so-called window method [7] to find out a better routing algorithm to generate particular permutation for associated networks. Deciding on whether an arbitrary permutation is admissible to a given MIN is still difficult problem.

Petri nets provide a framework for the modeling, simulation, specification and validation of dynamic systems. Petri nets are flexible specification languages for concurrent systems and offer wide range of applicability. Petri nets have been successfully employed in many scientific and industrial applications. In [1-2] CP-nets particularly have successfully been employed for analysis the permutation capability in MINs.

CP-nets are class of high level Petri nets which in turn evolved from finite state automata as specification for concurrent systems. The use of colors allows a convenient way to manipulate information that we have been able to exploit in modeling of MINs. The convenience is both in the size of the model, which becomes comparable to that of the MIN itself, and in easiness with which the behavioral features are captured.

In this work we propose a new approach to investigate the permutation admissibility, which is based on modeling of MINs with colored Petri nets (CP-nets). The main idea behind of the proposed approach is to reduce the permutation admissibility problem to the problem of reachability in CP-nets. There exist a plenty of well-established and easy-touse methods to analyze reachability in ordinary P/Tnets including reachability graph, coverability graph and place invariants methods. On the other hand, it is broadly-known that ordinary P/T-nets and CP-nets have the same computational power meaning that given a CP-net, one can construct an equivalent P/Tnet and vice versa. Once CP-net modeling of a MIN has completed we can further transform related CP-net to an equivalent P/T-net and use the method of place invariants to decide on permutation reachability in CP-net and herein decide on permutation admissibility to the given MIN.

The remainder of the paper is organized as follows. CP-net model of a MIN is presented in Section II. Colored place invariants are discussed in Section III. In Section IV colored place invariants are implemented to MINs. Finally, Section V is a conclusion.

### II. MIN CP-NET MODELING

As it is defined in [1-2] a CP-net of a MIN is 9-tuple CPN=(S,P,T,A,N,C,G,E,I), such that

- ❖  $\Sigma = \{INPUT, CONDITION\}$ , where  $INPUT = \{0,...,2^n 1\}, CONDITION = \{OK\};$
- $P = P_{IN} \cup P_{AUX} \cup P_{OUT} \cup P_{CDN}$ , where
  - $P_{IN} = \{IN_i\}, i = 1,..., 2^{n-1}, \text{ is the set of input places:}$
  - $P_{AUX} = \{AUX_i\}, i = 1,...,m2^{n-1}, \text{ is the set of }$ auxiliary places;
  - $P_{OUT} = \{OUT_i\}$ ,  $i = 1,...,2^n$ , is the set of output places;
  - $P_{CDN} = \{CDN_i\}, i = 1,...,(m+1)2^{n-1}, \text{ is the set of condition places};$
- $T = T_{SW} \cup T_{DP}$ , where
  - $T_{SW} = \{SW_i\}$ ,  $i = 1,...,m2^{n-1}$ , is the set of switch transitions;
  - $T_{DP} = \{DP_i\}$ ,  $i = 1,..., 2^{n-1}$ , is the set of display transitions;
- $\ \ \, \ \, A = A_{SW} \cup A_{PTN} \cup A_{CDN} \cup A_{DP} \, , \, \text{where} \,$ 
  - $A_{SW} \subseteq (P_{IN} \times T_{SW}) \cup (T_{SW} \times P_{AUX})$  is the set of switch arcs;
  - $A_{PTN} \subseteq (P_{AUX} \times T_{SW})$  is the set of pattern arcs:
  - $A_{CDN} \subseteq (P_{CDN} \times T) \cup (T \times P_{CDN})$  is the set of condition arcs;
  - $A_{DP} \subseteq (P_{AUX} \times T_{DP}) \cup (T_{DP} \times P_{OUT})$  is the set of display arcs;
- $C(p) = \begin{cases} CONDITION, & \text{if } p \in P_{CDN} \\ INPUT, & \text{otherwise;} \end{cases}$
- $G(t) = \text{true } \forall t \in T$ ;
- $\bullet \quad E(a) = \begin{cases} NEXT, & \text{if } a \in A_{CDN} \\ (Var(a))_{MS}, & \text{otherwise} \end{cases}$

where Type(Var(a)) = INPUT;

\* 
$$I(p) = \begin{cases} 1'(2i-2) + 1'(2i-1), & \text{if } p \in INi, i = 1, ..., 2^{n-1} \\ (empty, & \text{otherwise.} \end{cases}$$

In the above definition, the set of places P, the finite set of transitions T, the finite set of arcs A, and the node function N determine a directed graph representing the static structure of the CP-net. However, the finite set of non-empty types, called color sets  $\Sigma$ , the guard function G, the colour function G, and the arc expression function E determine the dynamic structure of the CP-net. I the initialization function is used to generate the initial marking which represents the initial state of the system. The sets P, T and A are pairwise disjoint i.e.

 $P\cap T=P\cap A=T\cap A=\emptyset$ . The node function  $N:A\to P\times T\cup T\times P$  associates each arc with a pair of nodes. Each arc connects a place p to a transition t or vice versa. The colour function  $C:P\to \Sigma$  assigns a colour set C(p) to each place p. The guard function G is defined from T into expressions such that

 $\forall t \in T : [\mathsf{Type}(G(t)) = \mathbf{B} \land \mathsf{Type}(\mathsf{Var}(G(t))) \subseteq \Sigma].$ 

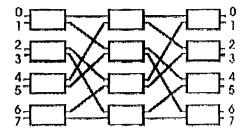


Figure 1.  $8 \times 8$  Omega network.

As an example of CP-net diagram of 8×8 Oniega network (Figure 1) is illustrated in Figure 2.

### III. COLORED PLACE INVARIANTS

Place invariants are equations that are satisfied in all reachable markings of the CP- net and are used to verify certain properties of the CP-net. The incidence matrix I of a CP-net contains a row for each place  $p \in P$  and a column for each transition  $t \in T$ . Each matrix element is defined by

$$I(p,t)(b) = E(t,p) < b > -E(p,t) < b >$$

where tokens are removed from and added in places according to the expressions E(t,p) < b > and E(p,t) < b >, respectively. In the above formula I(p,t)(b) describes how the marking of a place p is changed when the transition t occurs with a binding b.

The important ingredients of the place invariants are incidence matrix I, step Y and two markings  $M_0$  and M'. An incidence matrix I is another way to represent the structure of CP-net, which turns out to be convenient when we deal with calculation of invariants. Step Y is a collection of transitions for all enabled bindings. Finally,  $M_0$  and M' stand for the initial and the final markings. Analysis of reachability by the method of place invariants is described by the equation:

$$M' = M_0 + (I \circ Y)(p),$$

Clectarations

color INPUT = int with 0 . 7;

color CONDITION = with 0 K,

var N1, N2, N3, N4, N5, N6, N7, N8 : INPUT;

var NEXT : CONDITION:

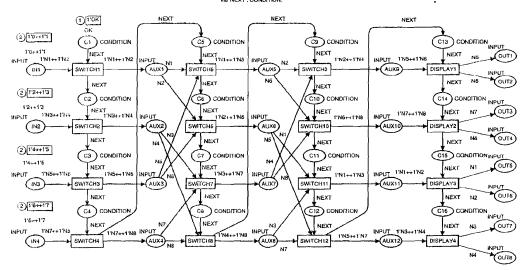


Figure 1. 8 × 8 Omega network CP-net diagram.

where

$$(I \circ Y) = \sum_{t \in T} \sum_{p \in Y(t)} I(p,t)Y(t)$$

$$= \sum_{t \in T} \sum_{p \in Y(t)} \sum_{b \in Y(t)} (E(t,p) < b > -E(p,t) < b >)$$

$$= \sum_{(t,b) \in Y} \sum_{p \in Y(t)} E(t,p) < b > -\sum_{(t,b) \in Y} \sum_{p \in Y(t)} E(p,t) < b >$$

### IV. CASE STUDY

As an example, let us consider the incidence matrix for 8×8 Omega network. The elements of related incidence matrix are linear functions:

$$\begin{split} &Id(1^{i}n_{i}++1^{i}n_{i+1})=1^{i}n_{i}++1^{i}n_{i+1},\\ &Fork(1^{i}n_{i}++1^{i}n_{i+1})=1^{i}n_{k},k\in\{i,i+1\}\\ &Join(1^{i}n_{i},1^{i}n_{j})=1^{i}n_{i}++1^{i}n_{j},n_{i}\neq n_{j}\\ &Cdn(1^{i}n_{i}+1^{i}n_{i+1})=1^{i}OK, \end{split}$$

where  $n_i, n_i, n_j, n_k \in C(p)$ ,  $1 \le i, j, k \le 8$ . The identity function Id is used to indicate the binding elements (transitions together with the surrounding arcs and arc expressions), which move tokens from

source places into destination places and keep the number and type of the tokens as they are. These binding elements appear in the left side of CP-net diagram of Figure 2. The binding elements in the middle of CP-net diagram (see Figure 2) are determined by the function Fork. The function Join represents binding elements that are arranged on the right side in Figure 2. Finally, the binding elements between conditional places and transitions (vertical arcs) are represented by the function Cdn. Zero function is denoted by an empty matrix entrance. Corresponding incidence matrix is shown in Table 1. In order to increase the readability, we list the places together with their color sets at the left of the table and transitions together associated bindings at the top.

In order to perform fully automatic calculation of colored place invariants we need to write down all place invariants, each representing a linear equation, represent CP-net as incidence matrix, and solve a homogeneous matrix equation. If the matrix equation has a solution (unique or multiple) then marking M is reachable from the initial marking, i.e.  $M \in R(M_0)$ . The non-existence of the solution indicates that M is

not reachable from the initial marking.

Table 1.  $8 \times 8$  Incidence matrix of Omega network CP-net.

					·	т			<del></del>	1			·		<del> </del>	T	
		INPUT	INPUT	INPUT	TUPUT	INPUT	INPUT	INPUT	INPUT	INPUT	INPUT	INPUT	INPUT	NPUT	NPUT	INPUT	INPUT
		SW1	SW2	SW3	SW4	SW5	SW8	SW7	SW8	SW9	SW10	SW11	SW12	DISPLAY1	DISPLAY2	DISPLAY3	DISPLAY4
INPUT	IN1	-Id	-			<u> </u>											
INPUT	IN2		-Id	<del> </del>	-		·		ļ	<del> </del>	<del> </del> -		<del> </del>		1	<del> </del>	1-
INPUT	IN3	<b></b>	<del> </del>	-Id	<del> </del>				<del>                                     </del>	<del> </del>		<b>—</b> ···	-	t -	<del> </del>	1	<del>                                     </del>
INPUT	IN4		<del> </del>	-	-ld				·	<b></b>				ļ			T-
INPUT	AUX1	1d	<del> </del>	-		-Fork	-Fork				<del> </del> -			<del>                                     </del>			1
INPUT	AUX2		ld					-Fork	·Fork	ļ			+	-		1	<del> </del>
INPUT	AUX3		1	ld	<del> </del>	-Fork	-Fork	<u> </u>		-				t			<del> </del>
INPUT	AUX4		<del> </del> -		īď			-Fork	-Fork		-	T		ļ			<del> </del>
INPUT	AUX5				<del>                                     </del>	Join				-Fork	-Fark	-		<del></del>			*******
INPUT	AUX6	<b> </b>					Join				-	-Fork	-Fork	-	<u> </u>		t
INPUT	AUX7	1			ļ———	l		Join		-Fork	-Fork			<u> </u>			
INPUT	AUX8	ļ						_	Join			-Fork	-Fork		1		
INPUT	AUX9		-							Join				-ld	1	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
INPUT	AUX10	<b></b>									.lojn		ļ- <del></del>		-td	i	-
INPUT	AUX11					<u> </u>						Join				-ld	
INPUT	AUX12												Join	_			-1d
INPUT	OUT1													Fork	-	<del>  ••••••</del>	
INPUT	OUT2		<del> </del>								 			Fork			
INPUT	OUT3														Fork		
INPUT	OUT4				[										Fork		
INPUT	OUT5							_								Fork	
INPUT	OUT6															Fork	
INPUT	OUT7																Fork
INPUT	OUT8																Fork
CONDITION	C1	-Cdn			1												
CONDITION	C2	Cdn	-Cdn														
CONDITION	C3		Cdn	-Cdn													
CONDITION	C4			Cdn	-Cdn												
CONDITION	C5				Cdn	-Cdn											
CONDITION	C6					Cdn	-Cdn										
CONDITION	C7				L		Cdn	-Cdn									
CONDITION	C8							Cdn	-Cdn								
CONDITION	C9								Cdn	-Cdn							
CONDITION	C10									Cdn	-Cdn						
CONDITION	C11										Cdn	-Cdn					
CONDITION	C12											Cdn	-Cdn				
CONDITION	C13												Cdn	-Cdn			
CONDITION	C14													Cdn	-Cdn		1
CONDITION	C15														Cdn	-Cdn	
CONDITION	C16															Cdn	-Cdn

A problem causes when we try to employ computer tools for automatic calculation of place invariants. Since the elements of incidence matrix are functions rather that numeric values, the method of colored place invariants is less useful for computerized analysis. Instead we would suggest CP-net unfolding and then verification of reachability in terms of place invariants for ordinary P/T-nets. Consequently an approach proposed in this work

#### V. CONCLUSION

In this paper we introduce a novel technique for analysis of permutation admissibility to a MIN. Based on this technique we can decide on permutation admissibility through analysis of reachability property of related CP-nets. The procedure passes through the following stages:

- · CP-net modeling of a MIN
- · Computing colored place invariants
- · Computing CP-net unfolding
- Computing place invariants
- Reachability analysis through matrix equations.

The main advantage of the proposed technique is that it allows us to perform fully automatic analysis of the objects.

### VI. REFERENCES

- [1] R. Bashirov, V. Crespi, "Analyzing Permutation capability of Multistage Interconnection Networks with Colored Petri Nets", Information Sciences, Elsevier, 176 (3), 2006, pp. 3143-3165.
- 170 (3), 2000, pp. 3175-310-3.

  [2] R. Bashirov, V. Crespi, "Quantitative Analysis of Permutation Capability with Colored Petri Nets", In: Proc. 13th IEEE International Conference on Modeling, Analysis and Simulation of Computer and Telecommunication Systems, IEEE Press, Atlanta, 27-29 September, 2005, pp. 463-470.
- [3] Q. Hu, X. Shen, W. Liang, "Optimally Routing LC Permutations on k-Extra-Stage Cube-Type Networks", *IEEE Transactions on Computers*, 45 (1), 1996, pp. 97-103.
- [4] S. T. Huang, S. K. Tripati, N. S. Chen, Y. C. Tseng, "An Efficient Roauting Algorithm for Realizing Linear Permutations on p'-Shuffle-Exchange Networks", IEEE Transactions on Computers, 40 (11), 1991, pp. 1292-1298.
- [5] J. Lenfant, "Parallel Permutations of Data: A Benes Network Control Algorithm for Frequently Used Permutations", *IEEE Transactions on Computers*, 27, 1978, pp. 637-647.
- [6] D. Nassimi, S. Sahni, "Parallel Algorithms to Set Up the Benes Permutation Network", *IEEE Transactions* on Computers, 31, 1983, pp. 148-154.

- [7] X. Shen, "An Optimal "O(NlgN) Algorithm for Permutation Admissibility to Extra-Sage Cube-Type Networks", *IEEE Transactions on Computers*, 44 (9), 1995, pp. 1144-1149.
- [8] X. Shen, F. Yang, Y. Pan, "Equivalent Permutation Capabilities Between Time-Division Optical Omega Networks and Non-Optical Extra-Stage Omega Networks", *IEEE Transactions on Computers*, 9 (4), 2001, pp. 518-524.
- [9] G. Veselovsky, "On BPC Permutations Admissibility to R-path Omega Networks", In: Proc. International Conference on Distributed and Parallel Processing Techniques and Applications, Las Vegas, 2004, pp. 1107-1113.
- [10] G. Veselovsky, "An Approach for Permutation Admissibility to a Binary hypercube under e-Routing", In: Proc. International Conference on Distributed and Parallel Processing Techniques and Applications, Las Vegas, 2005, pp. 95-104.